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Reliability Statistics of Quartzdyne® Pressure Transducers

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Introduction

This report describes the general design of a Quartzdyne® Pressure Transducer, and provides updated reliability statistics and common failure modes for each of the major components, based on field-failure returns. Our statistics suggest that the pressure crystal is the most reliable component of the transducer, and we discuss initiatives to further improve the product reliability in the future.

Transducer Design

All Quartzdyne Pressure Transducers share three common elements: a set of three quartz crystals, an oscillator circuit, and an isolation bellows. Although various models offer different packaging options (i.e., 1", 7/8", 3/4", 1/2"), a straightforward analysis of customer returns indicates that the ultimate reliability of our products lies in the performance of these core elements.

The quartz pressure and temperature crystals change frequency to measure pressure and temperature; the quartz reference crystal is used as a clock. All three crystals are temperature-sensitive in varying degrees, but only the pressure crystal is hydrostatically exposed to pressure. As depicted in Figure 1, the temperature and reference crystals are of similar construction (packaged in metal TO-5 cans), while the pressure crystal is a monolithic quartz crystal, capable of withstanding high compressive stresses.

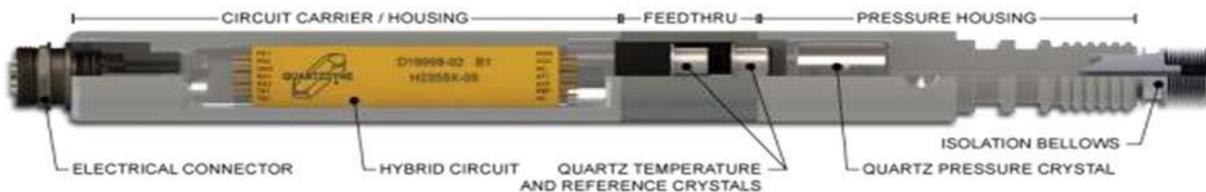


Figure 1: Cross-Section of Typical Quartzdyne Pressure Transducer with Key Elements Identified

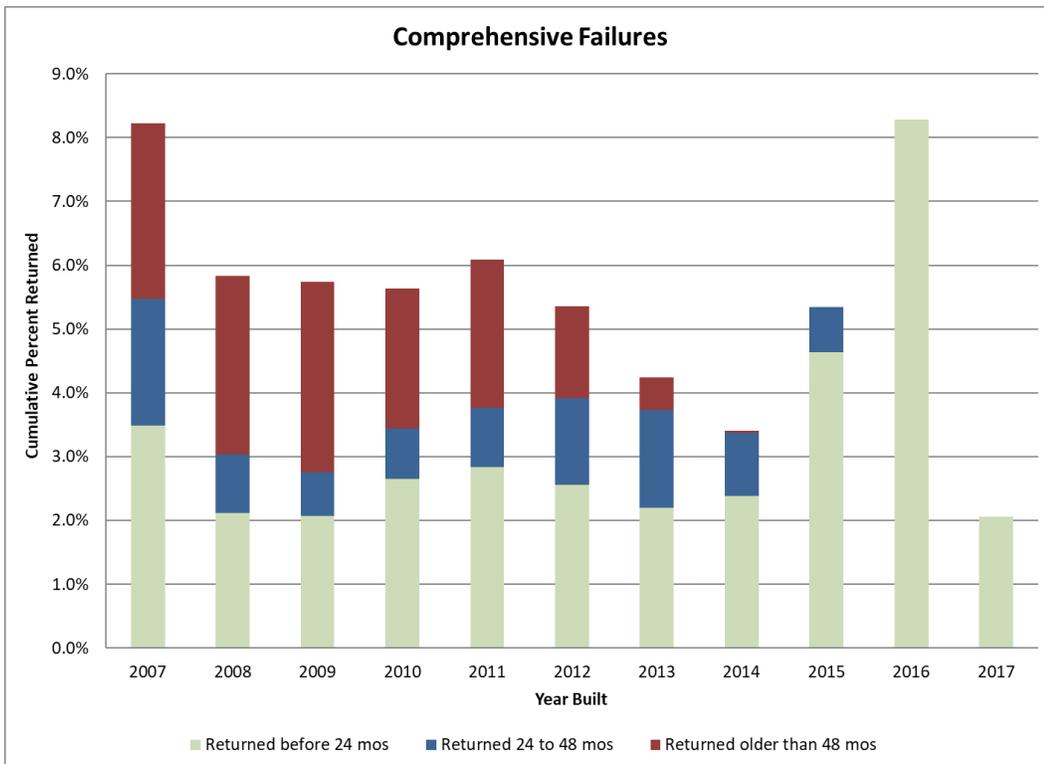


Figure 2: Overall Reliability

Overall Reliability

Quartzdyne utilizes two data sources to track product reliability: internal testing and customer returns/feedback. Internal tests are designed to test a specific element (crystal, circuit, bellows) over a range of operating conditions. This requires several customized tests, each optimized to probe the element in question for specific failure criteria. Customer returns provide valuable feedback, revealing which areas need the most improvement and where internal testing should be improved.

We maintain an extensive database containing measurements from all internal tests and customer returns. Continuous analysis of this data and then focusing on the "weakest link" has led to significant improvements over the past fifteen years. For Quartzdyne, continuous improvement has increased the working life of each transducer.

Therefore, a thorough review of the field performance of Quartzdyne Pressure Transducers helps us chart our progress towards this goal. Publishing field reliability data, both the good and the bad, is rare in our industry. We do it to establish customer confidence in our ongoing work of continuous improvement.

Quartzdyne sells transducers into three primary markets: logging/wireline, drilling, and permanent monitoring. Since transducers sold to permanent monitoring applications rarely return to Quartzdyne, we do not include transducers sold to this market in the denominator. Consider Figure 2, where the cumulative percent returned is defined as:

$$\frac{\text{Number returned from a given year (Logging, Drilling, and Permanent)}}{\text{Number of New units shipped in a given year (Logging and Drilling)}}$$

These percentages are charted versus the age (at return) of the transducer, and sorted by the year built. For example, as shown in Figure 2 the "average" transducer built in 2006 had a 5.4% failure rate after two years in the field, while the "average" 2007 transducer had a 4.5% failure rate after two years in the field. This chart does NOT provide any information concerning the actual time at temperature and pressure. Most of these transducers have been placed downhole in many different locations, and our customers do not provide us a log.

The overall reliability chart shown in Figure 2 includes all failures traced to a Quartzdyne component (crystals, circuit, bellows, etc.) We excluded all returned transducers that qualified for one of the following criteria:

- failed due to mishandling (dropped downhole, crushed, flooded, etc.)
- returned for recalibration
- returned for a circuit or model upgrade without any failure symptom noted

To focus our improvement efforts, the comprehensive failures are split into categories: pressure crystals (QP), temperature crystals (QT) and reference crystals (QR), circuits (surface-mount or hybrid), bellows, and miscellaneous (dimensional, wires, screws, incorrect documentation, packaging).

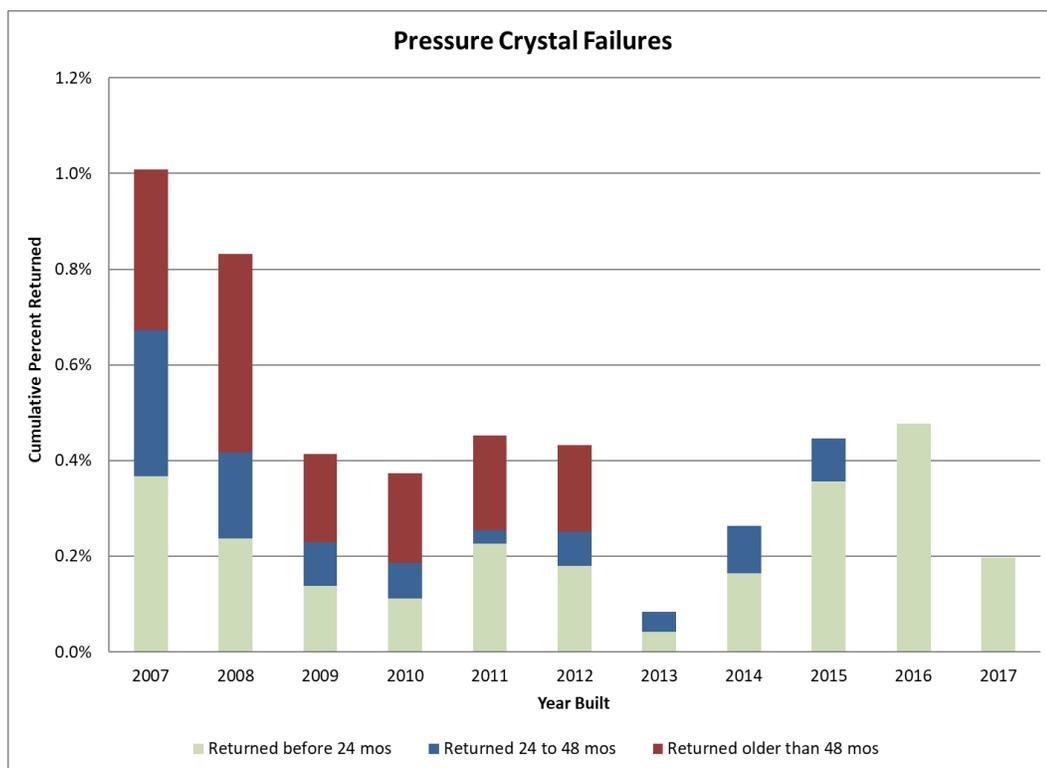


Figure 3: Pressure Crystal Reliability

Pressure Crystals

Historically, quartz pressure (QP) crystal failure rates were around one percent returned. Drive Level Sensitivity (DLS) accounts for roughly 50% of pressure crystal failures. DLS is quantified by measuring the impedance of the resonant frequency at various drive (energy) levels. A set of stable impedance measurements over the drive level set is an indication of an electrically "good" pressure crystal. DLS crystals may exhibit slow or non-startup. DLS may be exacerbated by mechanical shock which could result in a marginal but functioning crystal going intermittent or completely "dead" in the field.

The second most frequent failure mode of pressure crystals (~40%) is high impedance (Hi-Z). Since the oscillator circuit is designed to drive pressure crystals with a specific range of impedances, a Hi-Z crystal simply will not start to oscillate. Hi-Z is often the result of a detached or broken electrical connection from the feed-through to the crystal.

A dramatic decrease in pressure crystal failures occurred in 1998, not shown in these charts. (For reference, 11% of pressure crystals manufactured in 1997 failed after four years in the field; current failure rates are less than 0.4 %.) We attribute this marked improvement to a more repeatable process and to an improved high-temperature design. The lead-attach process change implemented in 2008 was thoroughly qualified, and in conjunction with the ASIC release (discussed below) resulted in a step improvement.

Transducers returned for repair are diagnosed; if the pressure crystal is not within the current specifications, Quartzdyne will not repair the transducer. Since circuit and crystal specifications are subject to change, the

crystal must be compatible with circuits available for repair. Scrapping an out-of-spec pressure crystal protects customers from the higher probability of experiencing a future field failure.

Because a pressure crystal failure results in a scrapped transducer, it is the most expensive failure mode (circuits, temperature, and reference crystals can be replaced). Accordingly, if a transducer is returned with a faulty circuit, but the pressure crystal is also Hi-Z, for example, the failure mode is assigned to the pressure crystal. As such, some of the transducers in Figure 3 may have failed initially for another symptom.

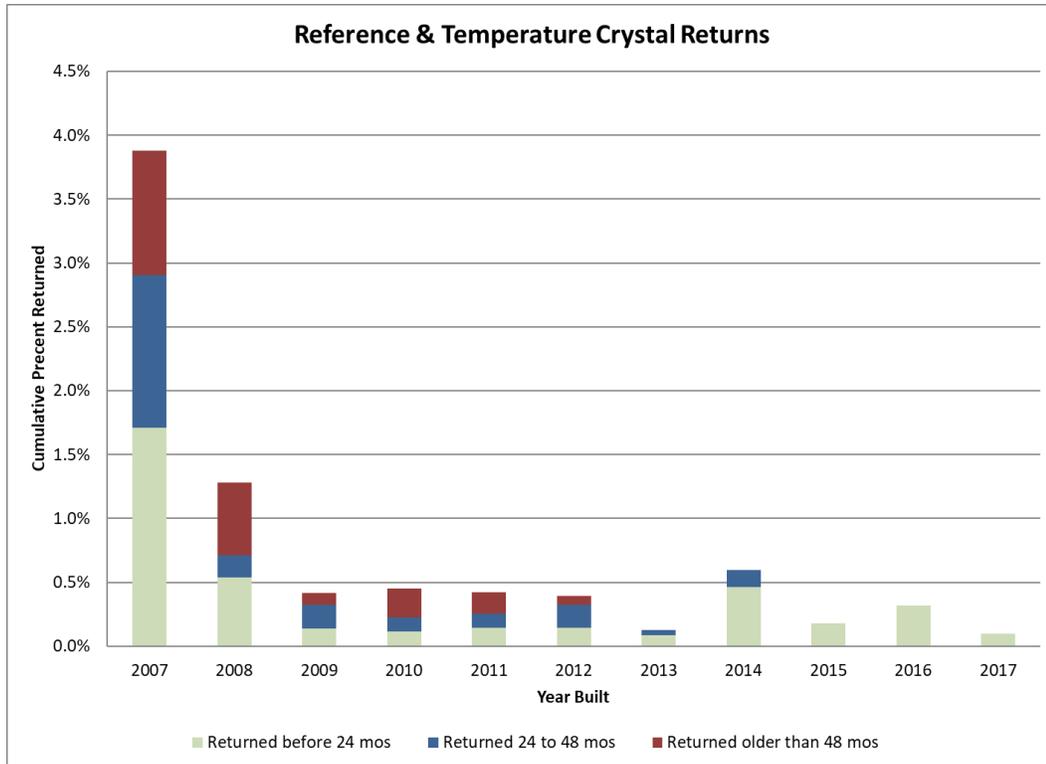


Figure 4: Temperature and Reference Crystal Reliability

Temperature and Reference Crystals

When discussing failures of the quartz temperature (QT) and reference (QR) crystals, the same terminology discussed in the Pressure Crystal section applies, namely DLS and Hi-Z. DLS and Hi-Z failures are often caused by severe shocks, where contaminants become dislodged, or the epoxy joints completely fracture. (The conductive epoxy is both the electrical and mechanical attachment of the crystal disc to the metal pins of the TO-5 can.) To make QT/QR crystals that survive drilling vibration, we stiffened the mounting joints inside the TO-5 can in 1999. This improvement has nearly eliminated Hi-Z failures since that time.

Through 2007, DLS remained the primary failure mode of our quartz crystals. We subsequently acquired a scanning-electron microscope (SEM) to help us better understand the source(s) of DLS. It is widely accepted that particle damping is a cause of DLS, and the SEM has helped us identify sources of microscopic particulates. We have attacked DLS failures in two ways:

1. Process Automation & Improvement. We introduced process automation initiatives during 2007 that have reduced batch variability and crystal handling. Automated systems remove variability and human interaction from the process. In 2010, we implemented cleaning schedules within the cleanroom to remove stray particles. Two years later, in 2012, we increased the scheduled maintenance frequency for the fixturing. The same year, several superfluous steps were removed from the process requiring less handling resulting in less particles. At the beginning of 2014 we also replaced screwdriver-driven screws with thumb screws to avoid creating metal particles with screwdriver in fixtures. In conjunction with Lean manufacturing, we continue to evaluate process changes that facilitate further reliability improvements.
2. Automatic Gain Control. In 2007, we developed a new ASIC circuit that “kick-starts” marginally DLS crystals. This feature is termed automatic gain control (AGC). Our customized AGC works in this manner: if the crystal impedance is normal, the AGC remains off. If the crystal impedance becomes unstable (DLS), the AGC turns on and overdrives the crystal to maintain oscillation. After a DLS episode

is over, the AGC shuts off. We've seen a 90% reduction of failures due to DLS QT/QR crystals on units shipped with ASIC hybrids since 2007. (2008 included shipments of both types of circuits.)

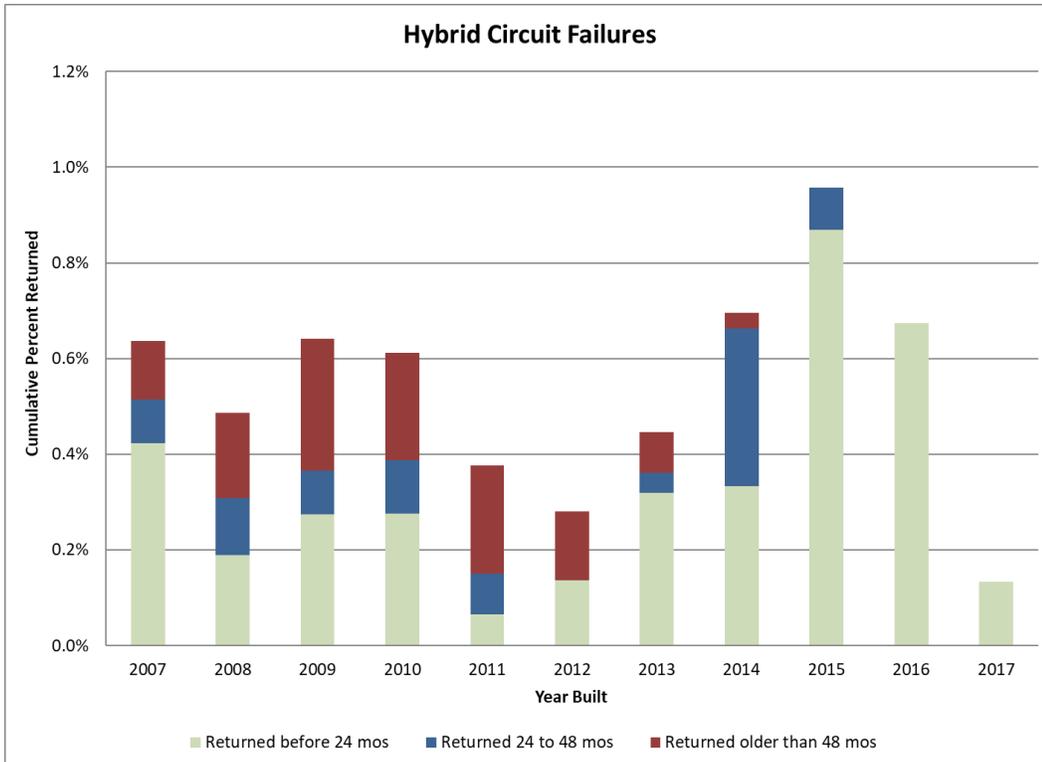


Figure 5: Hybrid Circuit Reliability

Circuits

Circuit life varies with electronic component packaging and the downhole environment (mainly temperature). Surface-mount (SMT) circuits can be used reliably at 125°C for one year, or at 100°C for five years. With properly constructed hybrid circuit assemblies (multi-chip modules), we expect more than five years at 200°C, or more than 30 years at 175°C.

Our circuit reliability is based upon our internal testing and field failures. Field failures generally corroborate with failure modes from our internal circuit-life tests. Since surface-mount and hybrid circuits are markedly different technologies, we split out the field-failures of these circuits in Figure 5 and Figure 8. Quartzdyne has written numerous papers on circuit technologies and test methods which are available on our website.

200°C-rated Hybrid Circuit

Quartzdyne introduced its hybrid circuit to the market in mid-2000. Our internal tests indicate that the hybrid circuit lasts 100 times longer than a SMT circuit, and over six times longer than a through-hole circuit. Due to its long-term reliability and robust design, the hybrid rapidly displaced the 177°C SMT and 200°C through-hole circuits.

Early failures of hybrid circuits were caused by electrostatic discharge (ESD), over-voltage, and customer miswiring with unlimited power supplies. ESD failures were addressed by customer notification and by using more robust components. Over-voltage and miswiring with unlimited supply current causes the I/O bondwires to melt; these failures require ongoing customer caution.

Mechanical integrity was another early source of hybrid circuit field failures. Thermal cycling coupled with vibration was suspected to weaken the substrate-to-package bond. Although the number of units returned for this symptom was few, we worked to improve the hybrid assembly in mid-2002. We also implemented the screening procedure described below on all hybrids. Since this time, this failure mode has nearly vanished from customer returns.

To minimize the number of infantile field failures, Quartzdyne screens each hybrid circuit prior to its usage in a product. The screen consists of fifteen (15) ½-hour thermal cycles between 25 and 225°C plus 72 hours aging at 225°C. Following the temperature cycling and aging, each hybrid is subjected to 10 mechanical shocks in a metal-to-metal drop fixture, which was designed for testing the robustness of the package and components under high shock loads. These tests are in addition to our lot-qualification test and the full-scale pressure/temperature calibration.

As mentioned in the QT/QR reliability section, all hybrids now include an AGC. Since an AGC would normally add dozens of components inside the hybrid (for which we had no room), we accomplished it by designing an Application Specific Integrated Circuit (ASIC). ASIC technology allows us to incorporate an AGC without increasing the size of the hybrid circuit. In fact, the oscillator ASIC has improved the reliability of the hybrid circuit as it reduces the total component count inside the hybrid by 50%. Since the ASIC operates on low voltage, it requires a 2.7 – 5.5 VDC supply.

To further reduce component count inside the hybrid, two new ASICs were developed during 2009 at Quartzdyne: a voltage regulator (Vreg) and a frequency counter. The Vreg ASIC enables our hybrids to operate down to a 2.7 VDC minimum supply, and the FC ASIC reduces current draw in digital-output hybrids and allowed us more functionality, such as a 5th byte checksum for data integrity. These two ASICs do the combined functions of 16 discrete components.

Figure 5 shows the hybrid circuit reliability. Note that hybrids operate in harsher environments than SMT circuits, and their reliability surpasses that of SMT. Hybrid returns are well less than 1% since 2007 when the ASIC oscillator was introduced.

We continue to improve our products by developing application-specific integrated circuits (ASIC) that make our electronics more reliable, higher temperature, and smaller. Our latest ASIC combines three ICs and 23 passives into one chip, reducing the total component count inside the frequency-output (“analog”) hybrid from 29 to 4! This breakthrough took several years to engineer, with a few “re-spins” to fine-tune the ESD protection and crystal oscillation. Due to the reduced component count, the analog circuit has shrunk to one-third of its prior length. Similar results were accomplished for the I2C-output (“digital”) hybrid. The new ASIC helped reduce the component count and size by 50%. The reduced component count and improved packaging also reduced the number of gold wirebonds, a known weak point in the hybrid(s). The new ASIC has survived over 20,000 hours at 275°C, and the accompanying transducers have passed our standard environmental qualification tests. See Figure 7.

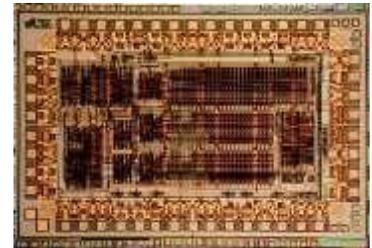
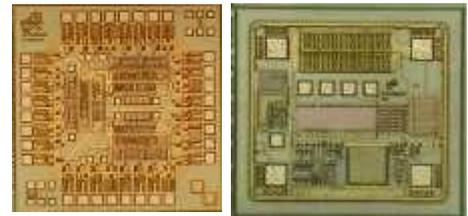


Figure 6: Oscillator, Voltage Regulator and Frequency Counting ASICs improved circuit and sensor reliability.

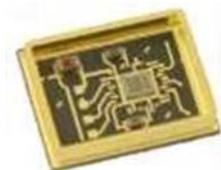


Figure 7: Next generation hybrid circuit. New ASIC eliminates 23 additional parts.

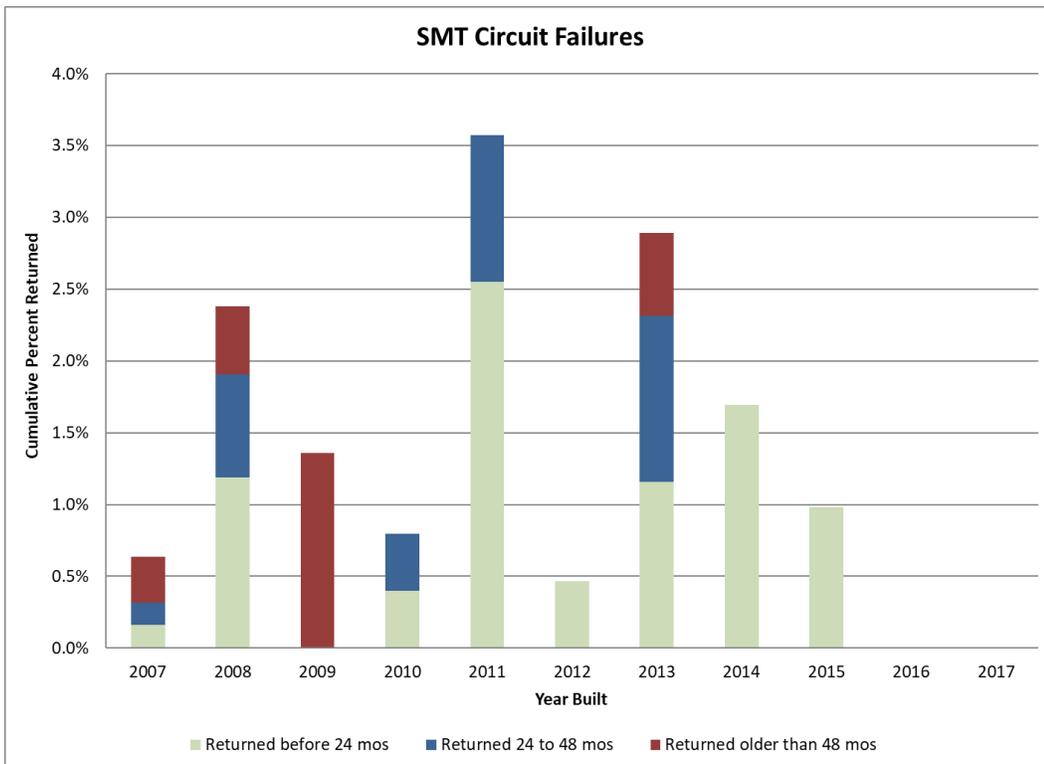


Figure 8: SMT Circuit Reliability

150°C-rated Surface-Mount (SMT) Circuit

The Sn63Pb37 (183°C eutectic) solder limits the operating temperature of this design. Prior to 2004 we also offered a Pb-free SAC solder version of our circuit. While this circuit was rated to 180°C, its lifetime at temperature was only a few months. In 2002, hybrid circuits began to replace the 180°C SMT in most high temperature applications and a step improvement in our returns data was observed. We re-introduced RoHS compliant SAC solder to all our SMT products in 2012, but the temperature rating will remain at 150°C.

Commercial plastic packages are used in the SMT designs. The primary failure mode in these designs is wirebond degradation at the Au-Al interface on the chip. The failure rate is accelerated by the formulation of the plastic package which has changed over the years. In 1998 we saw failures at about 2400 hours at 180°C. This decreased to 600 hours by 2004 as smaller particle size and more active Bromine flame retardants were incorporated. New Halide-free plastic formulations have been introduced in response to the higher soldering temperatures required to satisfy the RoHS directive. These packages are demonstrating lifetimes not experienced since before 1998.

In 2005 most SMT returns were digital-output circuits that demonstrated a problem in the reset logic during startup. Some circuits did not startup properly which could result in a hung bus depending upon the customer's software. We did not discover the problem since our test systems were designed to detect the problem, reset the bus, and try again (per our manual and Phillips I2C recommendations). We revised the digital FPGA firmware in 2006 to eliminate the startup problem. Total SMT returns have been below 2.5% since 2006.

The 2011 increase was primarily due to solder failure on ICs. Most ICs are now supplied with Pb-Free plating which is more susceptible to wetting problems using Sn63 solder. Our internal tests have shown that moving to SAC solder addresses the solder problem and we therefore expect failure rates to decrease to historic 2008-2009 levels (or better).

No SMT returns were recorded in 2016/2017. See Figure 8.

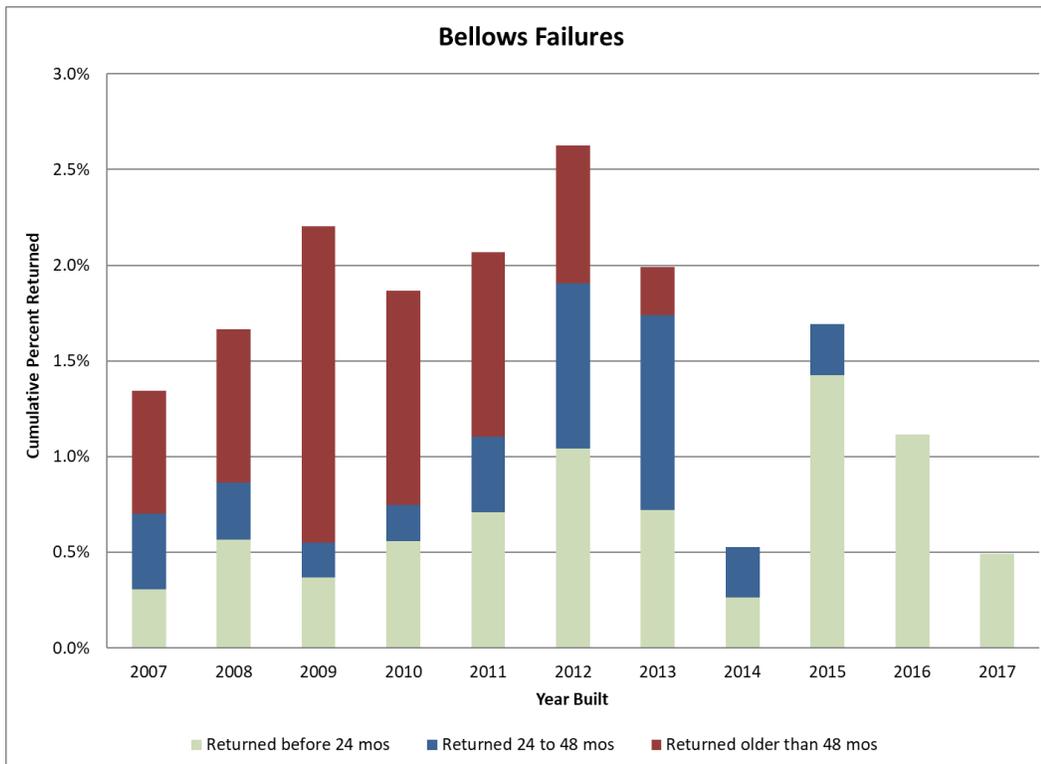


Figure 9: Bellows Reliability

Bellows Failures

In 2005, we became aware of sealing issues with our QMB bellows. This problem was addressed by moving to a larger thread which allowed more torque to provide a better seal – see Figure 10. This improvement was implemented in April 2007.

Another bellows failure mode is damage caused by particulates. We have made advances on diaphragm designs which may alleviate this problem – see Figure 11. We received a patent for a new diaphragm design that makes diaphragms possible even on our 3/4" OD products. Consult with our sales department to see if a diaphragm may be a good alternative to the traditional bellows in your application.

In 2013 we launched a major initiative to a reduce returns for bellows problems. The goal was to eliminate high pressure readings (+5 psi over normal) at ambient pressures. While accurate barometric pressure is not within our specified operating range of > 200psi to full scale, the phenomenon creates concerns for our customers who verify performance at the surface before deploying a tool. The results of this effort were a success. We were able, through process improvement during the bellows backfill process, to bring these readings back in-line with expectations.

In 2013 / 2014, due to feedback from the field, Quartzdyne studied incidences of loosened QMB (7/8") bellows. We know that bellows torque relaxes with time at temperature. However, over the previous 12 months customers reported problems with QMB bellows coming loose. Because of this information, we increased the installation torque of the bellows from 40 to 70 ft lbs. So far, internal testing and customer feedback has confirmed that this action corrected the problem.



Figure 10: Thread change provided increased reliability for bellows.



Figure 11: Dual Sided Oval Diaphragm (DSOD).

The 2015/2016 increase was largely due to returns for high or low-pressure readings when measuring at ambient conditions. We discovered that products exposed to high temperature without pressure can result in plastic deformation of the bellows. Heating the bellows expands the backfill fluid beyond the elastic range of the metal. When the bellows cool the initial bellows setpoint or height is different causing a high or low reading. This method of testing product goes beyond Quartzdyne's recommended usage. For more information on testing condition contact the Quartzdyne sales department.

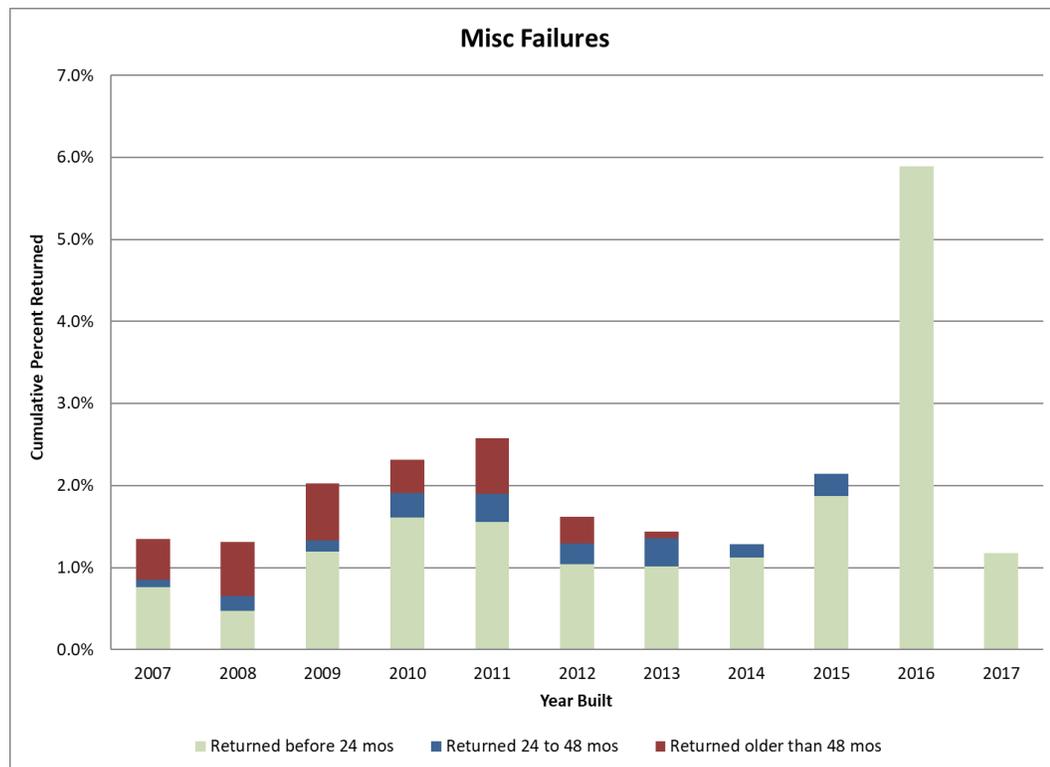


Figure 12: Miscellaneous Failures

Miscellaneous Failures

The 2016 increase in miscellaneous returns (4.5%) was primarily a combination of dimensional, contamination, recall, mechanical, process, and wiring issues. Almost half (2.2%) are attributed dimensional flaws, oversize or undersize dimensions, missing features, scratches, burrs, etc. 1% of the returns are attributed to a recall relating to a pressure crystal plating issue. After receiving the units none were found to have the plating problem that prompted the recall.

Several improvements were implemented in 2017 that directly address the uptick in miscellaneous failures. For dimensional returns, the method of identifying critical items was revised. Most critical parts now have part specific inspection guidelines and some have custom supplier quality plans that explain all critical to quality requirements to suppliers. We have also purchased new inspection instruments and equipment including an automated inspection machine. Overall, we have adopted a different and more detailed approach to inspection. In conjunction with incoming inspection improvements Quartzdyne launched product specific final inspection standards. Quartzdyne now has a cell with personnel dedicated to final inspection. Rejects are tracked and reported daily at the factory level. All nonconformities result in root cause and corrective action investigations. For contamination returns, most of which are flux failures resulting in no pressure and or no temperature readings, the cleaning procedure for soldered connections was revised. Also, all solder connections are peer inspected using a 100X magnification microscope. In addition, Quartzdyne has launched wire welded connections for most product types. Additional improvements were implemented including additional statistical process controls for critical processes to reduce rejects, rework, and overall process variability. Quartzdyne is committed reversing the increase in miscellaneous failures. For more information consult our sales department